

S/N 09/745,780

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Martin C. Roberts et al.

Examiner: W. David Coleman

Serial No.: 09/745,780

Group Art Unit: 2823

Filed: December 21, 2000

Docket: 303.451US6

Title: METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT
USING A DUAL POLY PROCESS

NOTICE OF APPEAL FROM THE DECISION OF THE EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

MAIL STOP AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

In compliance with 37 C.F.R. § 41.31(a)(1), Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated December 16, 2004, of the Examiner rejecting claims 38-45, 47, 53-56, 58, 60-62, and 68 of the above-identified patent application.

A request for an extension of time to respond to the Examiner's rejection is submitted herewith along with payment of the required extension fee.

Please charge the amount of \$500.00 in payment of the Notice of Appeal fee under 37 C.F.R. § 41.20(b)(1), as well as any additional required fees, to Deposit Account No. 19-0743.

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Respectfully submitted,

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Date

April 18, 2005

By

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to MAIL STOP AF, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 18, 2005.

Name

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Signature

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